

High Efficiency Plastic Rectifiers

Reverse Voltage: 50 to 1000V  
Forward Current: 1.0 Amp

Features

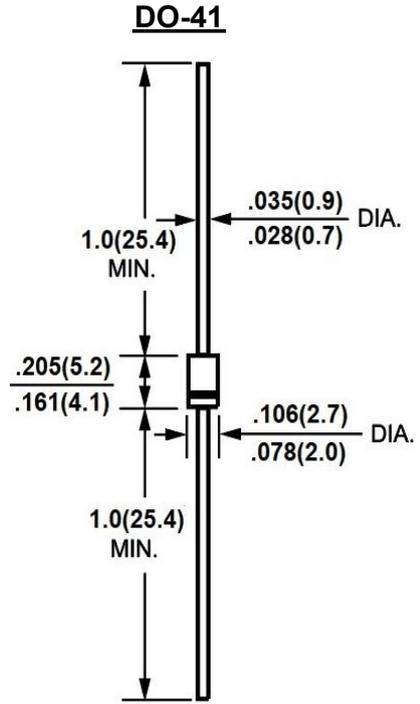
- Glass passivated chip junction
- High forward surge capability
- Super fast reverse recovery time
- Low reverse leakage
- High temperature soldering guaranteed  
260°C/10 seconds at terminals

Mechanical Data

- **Package:** DO-41, Molded plastic body  
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant
- **Terminals:** Tin plated leads, solderable per MIL-STD-750, Method 2026
- **Polarity:** Color band denotes cathode end

Typical Applications

For use in high frequency rectification of power supplies, inverters, converters, and freewheeling diodes for consumer and telecommunication.



Package Outline Dimensions in Inches (Millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter&Test Conditions	SYM.	HER 101	HER 102	HER 103	HER 104	HER 105	HER 106	HER 107	HER 108	Unit
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	300	400	600	800	1000	V
Maximum RMS Voltage	V <sub>RMS</sub>	35	70	140	210	280	420	560	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	300	400	600	800	1000	V
Average rectified output current @60Hz sine wave, Resistance load (FIG.1)	I <sub>O(AV)</sub>	1.0								A
Peak Forward Surge Current 8.3ms Single Half Sine-wave Superimposed On Rated Load	I <sub>FSM</sub>	30								A
Maximum Thermal Resistance, Junction To Ambient (Note1)	R <sub>θJA</sub>	60								°C/W
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to+150								°C

Electrical Characteristics (Ta=25°C Unless otherwise specified)

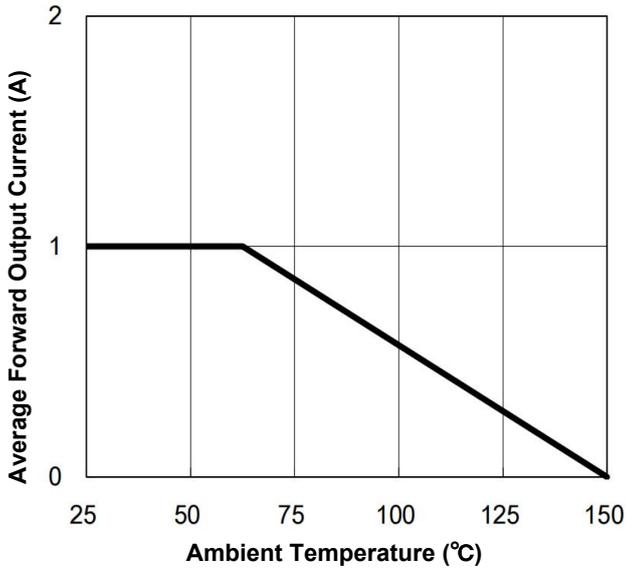
Parameter&Test Conditions	SYM.	HER 101	HER 102	HER 103	HER 104	HER 105	HER 106	HER 107	HER 108	Unit
Maximum Instantaneous Forward Voltage IFM =1A	V <sub>FM</sub>	1.0		1.3		1.7				V
Maximum DC reverse current at rated DC blocking voltage	I <sub>R</sub>	5								µA
		100								
Maximum reverse recovery time(Note 2)	T <sub>rr</sub>	50				75				ns
Typical junction capacitance(Note3)	C <sub>J</sub>	15		10		7				pF

Notes:

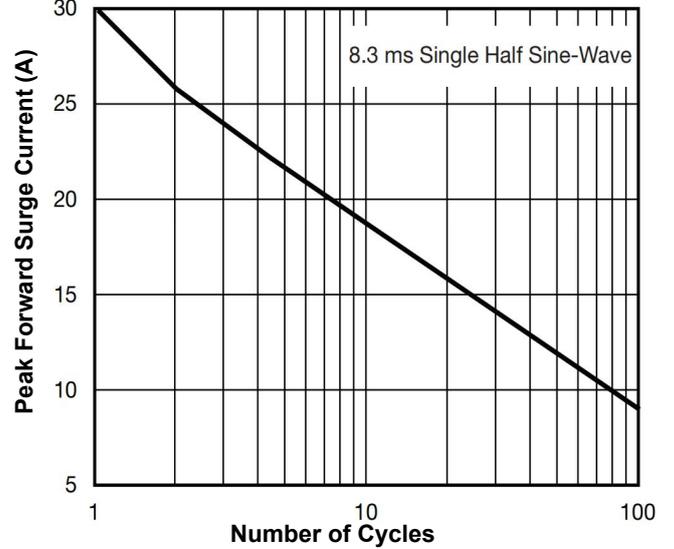
1. Thermal resistance from junction to ambient at 0.375" (9.5 mm) lead length, PCB mounted.
2. Reverse recovery time test condition: IF=0.5A IR=1.0A I<sub>rr</sub>=0.25A .
3. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
4. The typical data above is for reference only.

**Rating and Characteristic Curves**

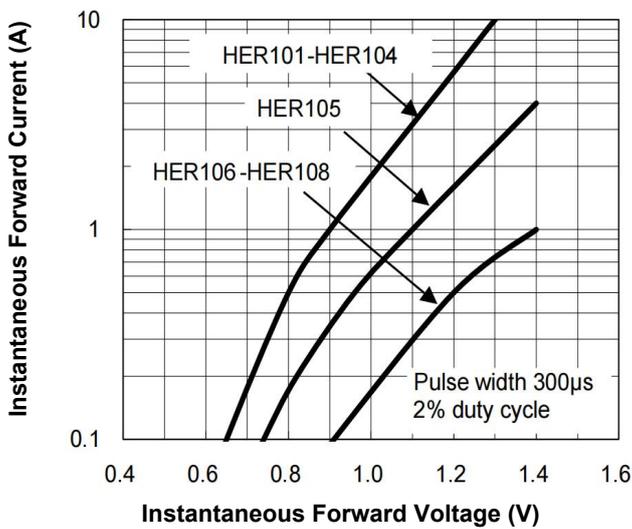
**FIG. 1- DERATING CURVE OUTPUT RECTIFIED**



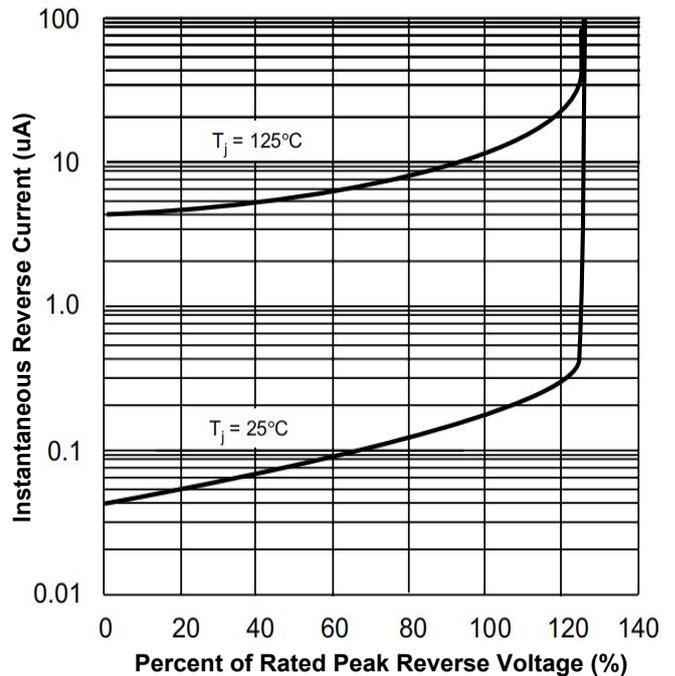
**FIG. 2- -MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT**



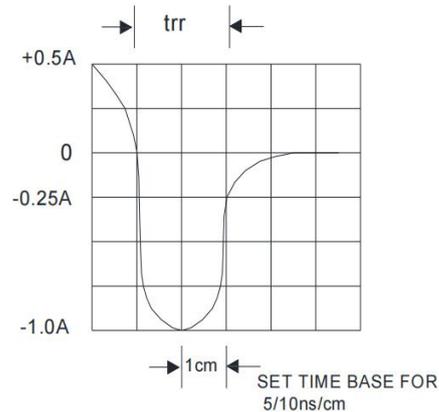
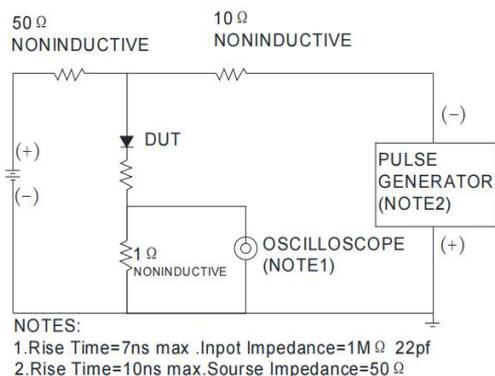
**FIG. 3- TYPICAL FORWARD VOLTAGE**



**FIG. 4- TYPICAL REVERSE LEAKAGE**



**FIG. 5- Diagram of circuit and Testing wave form of reverse recovery**



The curve above is for reference only.



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