



650V N沟道增强型超结功率场效应管 **650V N-CHANNEL Super-Junction POWER MOSFET**
14A/650V

Discription
 JXM65R280F This N-Channel enhancement mode power MOSFET is produced using advanced Super Junction technology. It achieves low conduction loss and switching losses. It leads the design engineers to their power converters with high efficiency, high power density, and superior thermal behavior. Furthermore, it's universal applicable, i.e., suitable for hard and soft switching topologies.

Features

- $V_{DS}=650V$, $I_D=14A$
- $R_{DS(on) (max.)}=280\ m\Omega@V_{GS}=10V$
- New revolutionary high voltage technology
- Ultra low gate charge
- High peak current capability

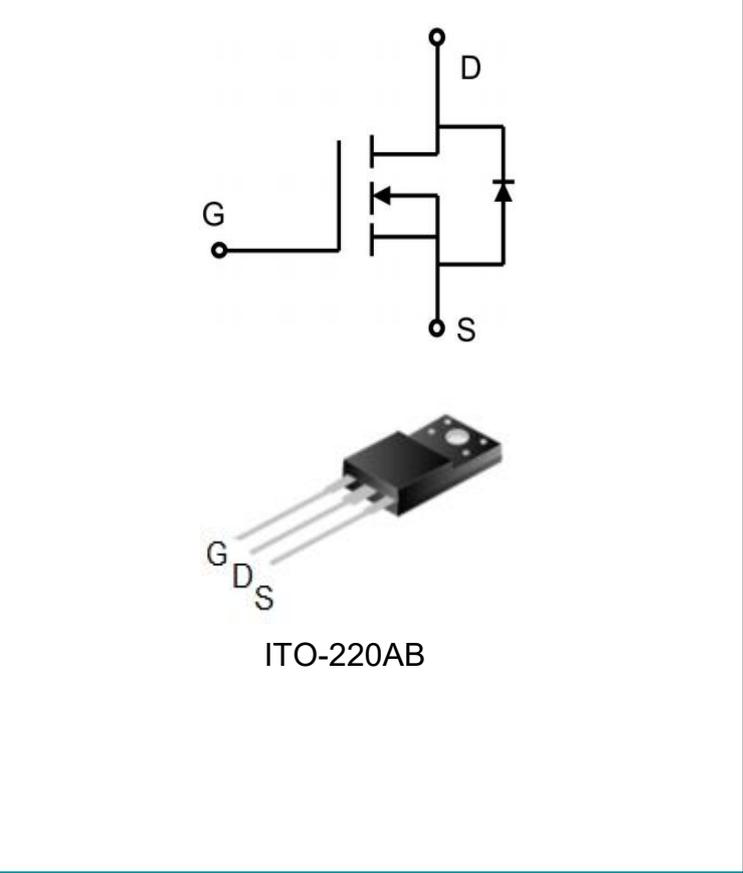
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100% ΔVds TESTED!

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LED lighting power

Note

- Products made by JUXIN semiconductor



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
JXM65R280F	ITO-220AB	JXM65R280F	Pb free	Tube

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltagee($V_{GS}=0V$)	V_{DS}	650	V
Gate-Source Voltagee	V_{GS}	±20	V
Gate-Source Voltagee($V_{DS}=0V$), AC($f>1HZ$)	V_{GS}	±30	V
Continuous Drain Current	I_D	$T_C=25^{\circ}C$	14
		$T_C=100^{\circ}C$	8.8
Drain Current Pulsed(Note 1)	I_{DM}	56	A
Power Dissipation ($T_C=25^{\circ}C$)	P_D	-Derate	25
			0.20
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	228	mJ
Reverse Diode dv/dt, $V_{DS}\leq 480V$ (Note 3)	dv/dt	15	V/ns
Drain Source Voltage Slope, $V_{DS}\leq 480V$	dVds/dt	50	V/ns
Operation Junction Temperature Range	T_J	-55 to+150	°C
Storage Temperature Range	T_{STG}	-55 to+150	°C

**THERMAL CHARACTERISTICS**

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W
Soldering Temperature, Wave Soldering only Allowed at Leads. (1.6mm from Case for 10s)	T_{sold}	260	°C

ELECTRICAL CHARACTERISTICS**Off Characteristics**

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Drain -Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	650	--	--	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V, T_J=25^\circ C$	--	--	1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=+20V, V_{DS}=0V$	--	--	100	nA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=-20V, V_{DS}=0V$	--	--	-100	nA

On Characteristics

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	2.0	3.5	4.0	V
Static Drain- Source On State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=7.0A$	--	240	280	m Ω
Gate resistance	R_g	$f=1MHz$	--	2.9	--	Ω

Dynamic Characteristics

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C_{iss}	$V_{DS}=100V, V_{GS}=0V, f=1MHz$	--	920	--	pF
Output Capacitance	C_{oss}		--	41	--	
Reverse Transfer Capacitance	C_{rss}		--	1	--	

Switching Characteristics

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=325V, V_{GS}=10V, R_G=24\Omega$ $I_D=14A$ (Note 4,5)	--	19	--	ns
Turn-on Rise Time	t_r		--	44	--	
Turn-off Delay Time	$t_{d(off)}$		--	68	--	
Turn-off Fall Time	t_f		--	36	--	
Total Gate Charge	Q_g	$V_{DD}=520V, V_{GS}=0$ to 10V, $I_D=14A$ (Note 4,5)	--	26	--	nC
Gate-Source Charge	Q_{gs}		--	7.2	--	
Gate-Drain Charge	Q_{gd}		--	12	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	14	A
Pulsed Source Current	I_{SM}		--	--	56	
Diode Forward Voltage	V_{SD}	$I_S=14A, V_{GS}=0V$	--	0.9	1.4	V
Reverse Recovery Time	T_{rr}	$I_S=14A, V_{GS}=0V, di/dt=100A/\mu S$	--	266	--	ns
Reverse Recovery Charge	Q_{rr}		--	3.4	--	μC

注:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting $T_J=25^\circ C$, $V_{DD}=50V$, $V_G=10V$, $R_G=25\Omega$.
3. $I_{SD} \leq I_D$, $di/dt \leq 200A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ C$.
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycles $\leq 2\%$.
5. Essentially Independent of Operating Temperature Typical Characteristics.

典型特性曲线

Fig.1: Output Characteristics

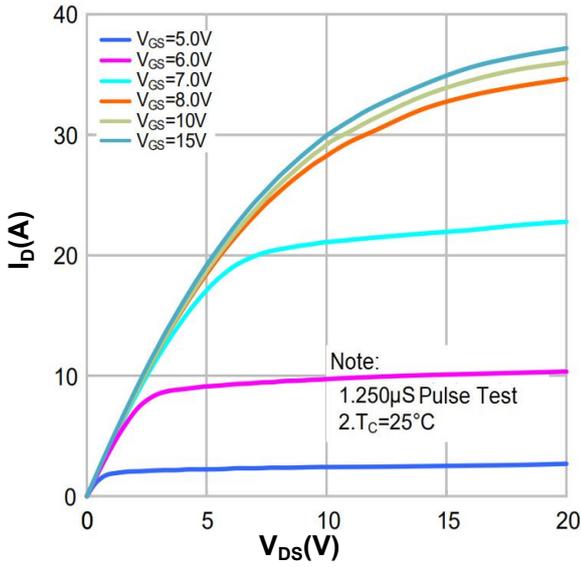


Fig.2: Output Characteristics

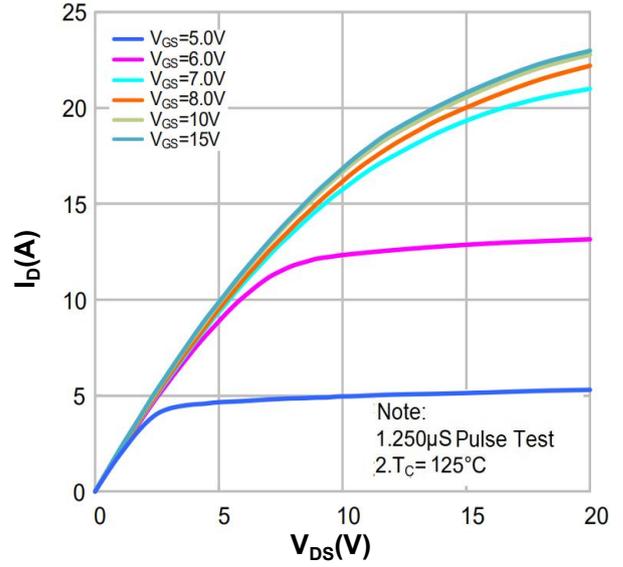


Fig.3: Typical Transfer Characteristics

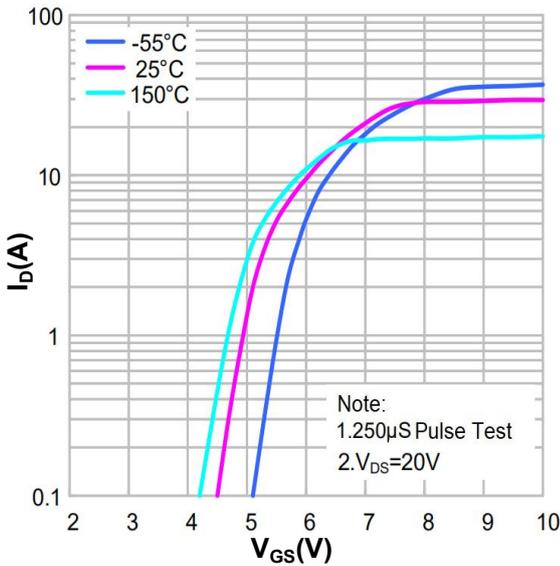


Fig.4: Typical On-resistance vs. Drain Current

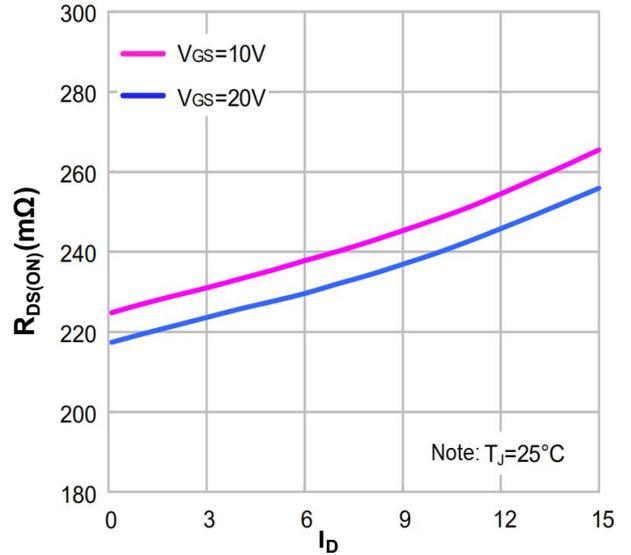


Fig.5: Typical On-resistance vs. Drain Current

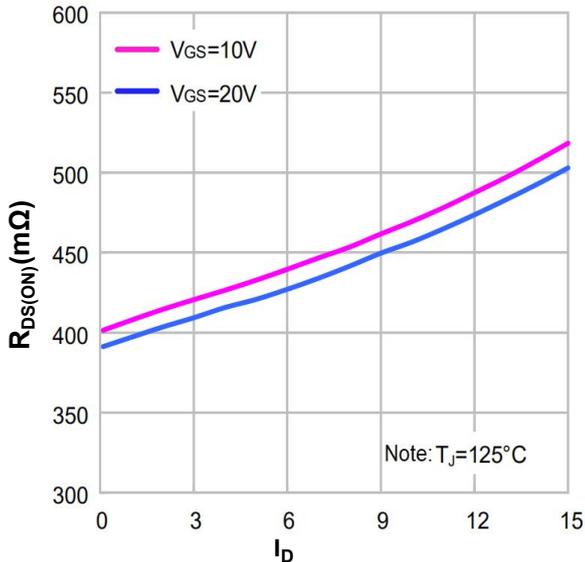
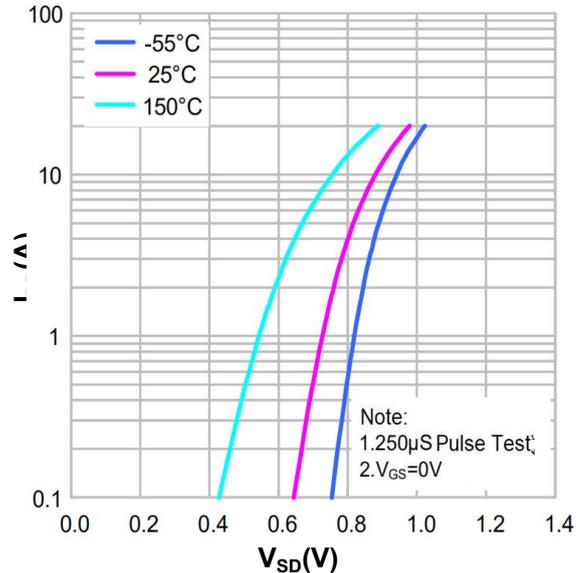


Fig.6: Typical Body Diode Forward Voltage



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Fig.7: Typical Capacitance Characteristics

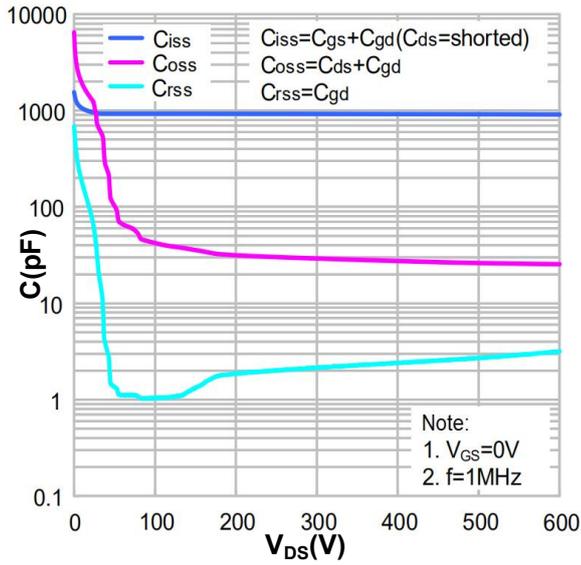


Fig.8: Typical Gate Charge Characteristics

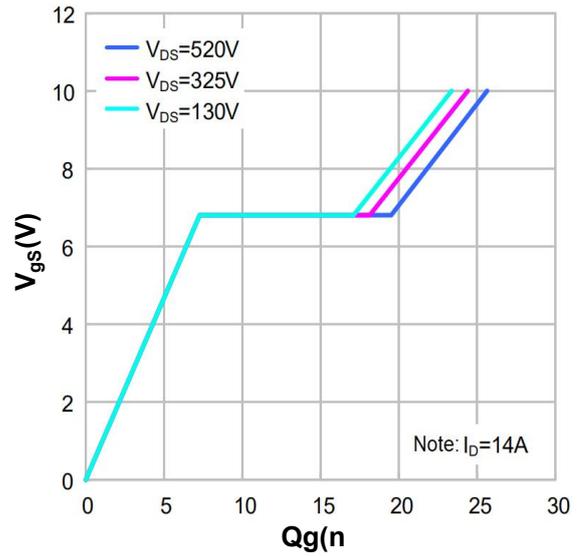


Fig.9: Normalized Breakdown Voltage vs. Junction Temperature

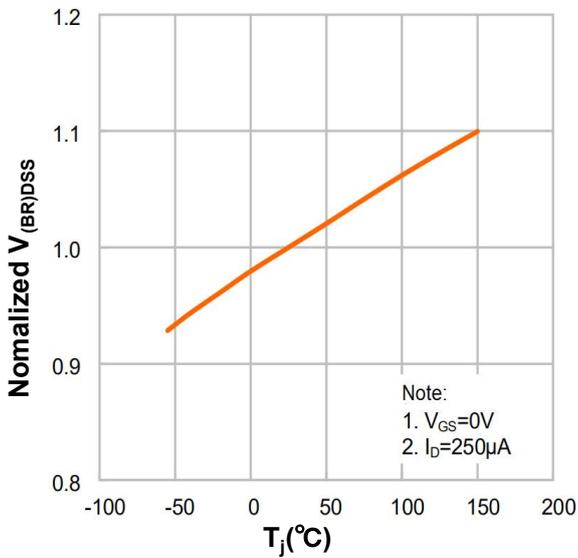


Fig.10: Normalized Breakdown Voltage vs. Junction Temperature

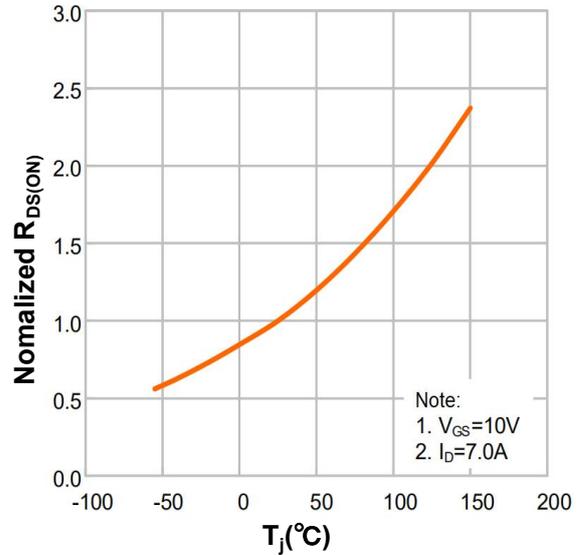


Fig.11: Maximum Safe Operating Area

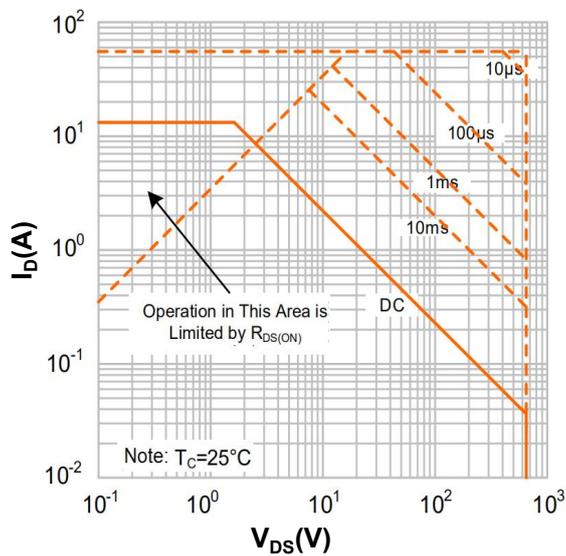
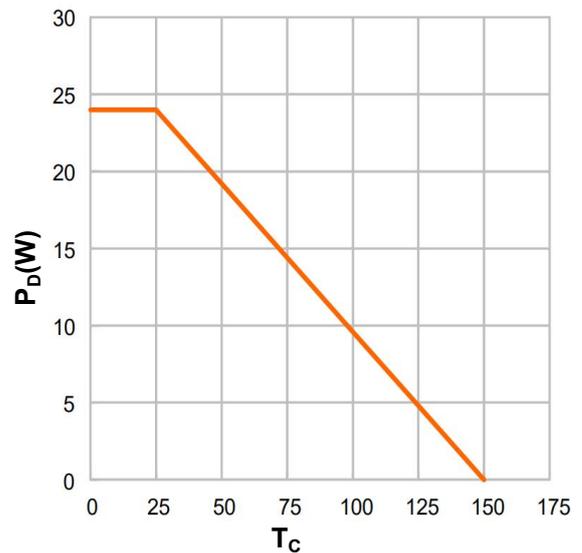


Fig.12: Power dissipation



The curve above is for reference only.

典型特性曲线

Fig.13: Maximum Drain Current vs. Case Temperature

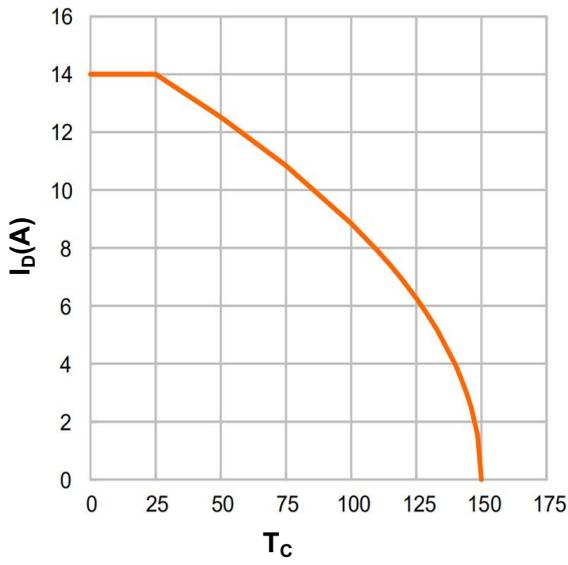
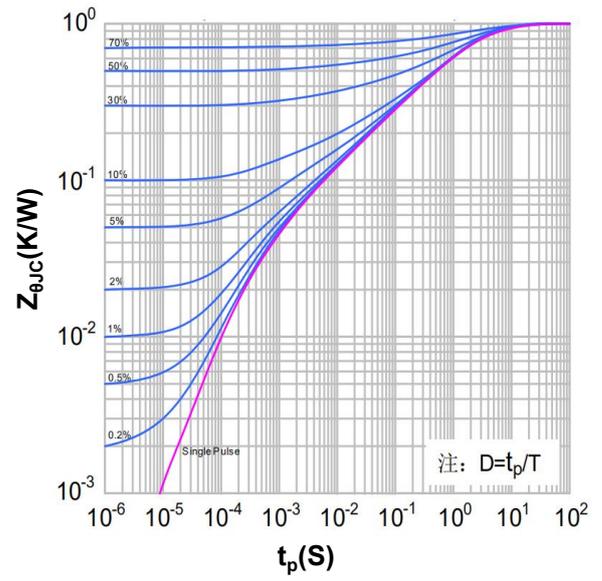
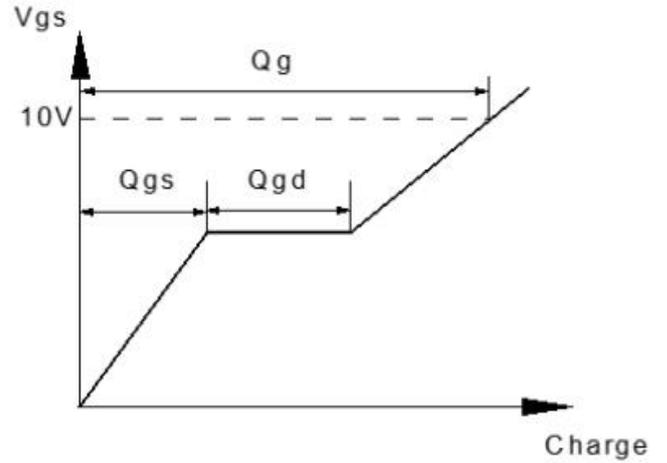
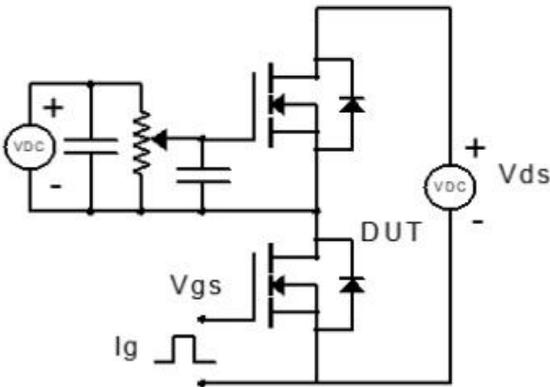


Fig.14: Transient Thermal Response Curve

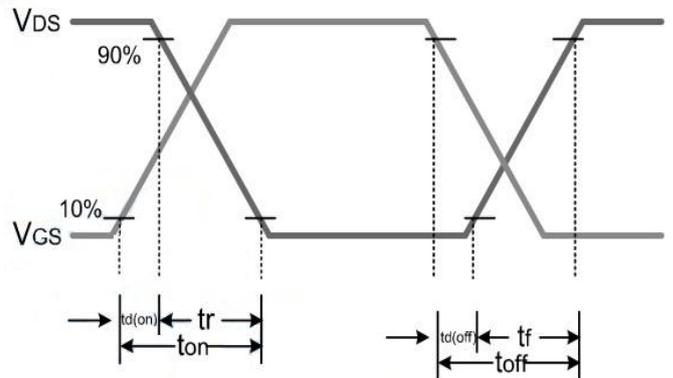
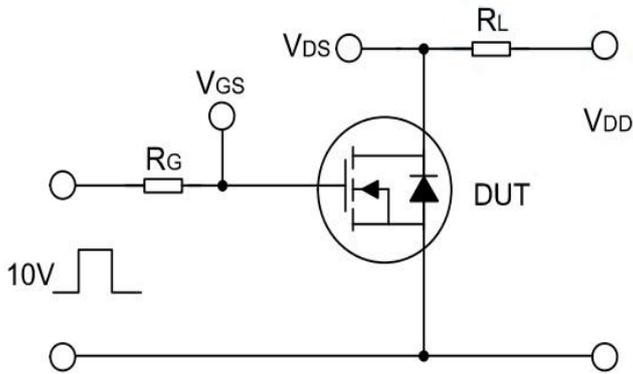


Test Circuit

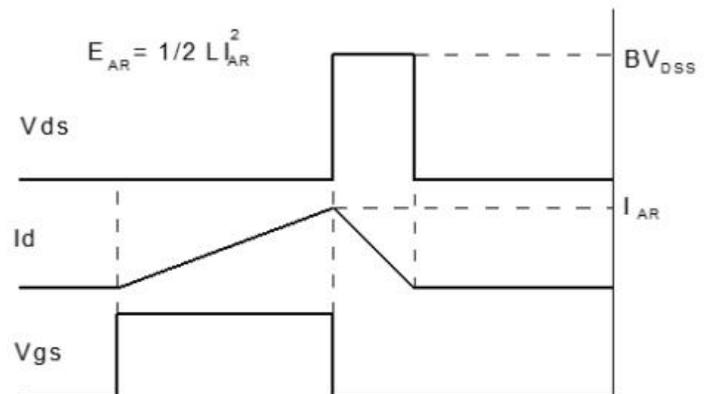
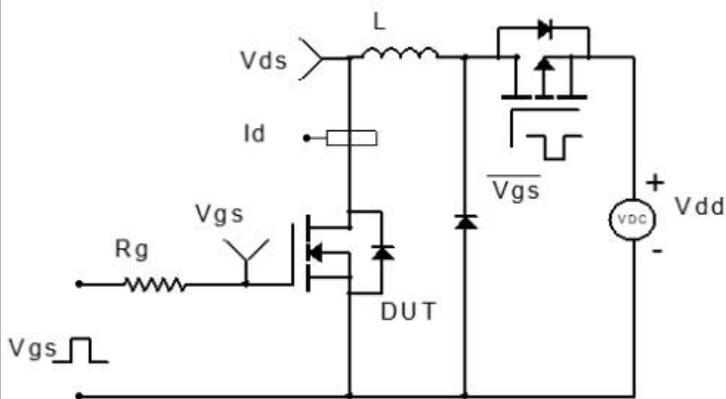
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



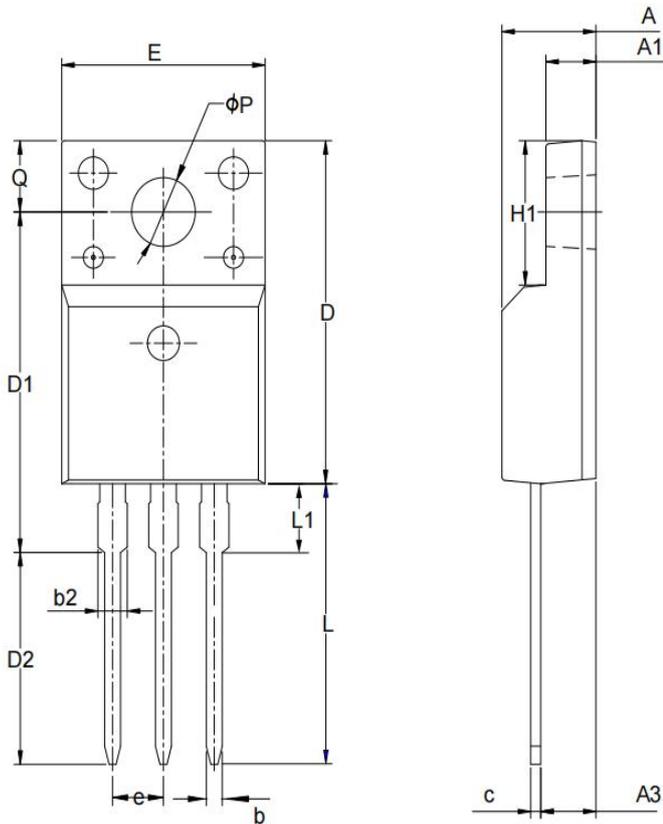
Unclamped Inductive Switching Test Circuit & Waveform



Package Dimensions of ITO-220AB

Package Dimensions of ITO-220AB

Unit:mm



SYMBOL	MILIMETER	
	MIN	MAX
A	4.20	4.95
A1	2.24	3.10
A3	2.30	3.30
b	0.60	1.00
b2	1.15	1.55
c	0.35	0.65
D	14.80	16.40
D1	15.00	17.00
D2	8.80	11.60
E	9.70	10.60
e	2.54BSC	
H1	6.00	7.40
L	11.40	15.10
L1	2.60	4.50
Φ P	2.90	3.40
Q	3.00	3.70

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